Remarks/Arguments

The Examiner has pointed out that U.S. Patent 6,263,396 to Cottle et al. teaches transferring the VBV buffer from SDRAM 312 to an operational memory on extension bus 300 in order to make room for OSD data. This is discussed in column 18, lines 33-40. Cottle also teaches that OSD data may be stored in an external memory attached to extension bus 300 as set forth in column 10, lines 21-23. However, nowhere does Cottle et al. teach or suggest the transfer of OSD data between memories. Rather, Cottle teaches that RAM 220 will turn on the OSD function and specify how and where OSD will be mixed and displayed as explained column 10, lines 22-26.

Nowhere does Cottle et al. teach or suggest that a

"memory for storing video data is (also) used to store OSD data that is no longer being displayed",

as specifically recited in Claim 1. Rather, Cottle et al. store OSD data either in SDRAM 312 or in an external memory attached to extension bus 300. Cottle et al. does not teach or suggest the transfer of OSD data from one memory to another, much less, the transfer of OSD data that is no longer being displayed to a first memory which stores video data, as specifically recited in Claim 1.

It is therefore clear that Cottle et al. utilize OSD data from whatever source holds such data, and that Cottle et al. do not transfer OSD data from one memory to another, as specifically recited in Claim 1.

Claims 2, 4 and 5 are dependent from Claim 1 and contain further advantageous features. The Applicants submit that the subclaims are patentable as their parent Claim 1.

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Claim 6 recites the method steps of:

"transferring OSD data that is no longer being displayed to the first memory which is used for video compression and upon request transferring back OSD data from the first memory to the second memory".

Nowhere does Cottle et al. show or suggest transfer of OSD data between memories, as explained above. It is therefore clear that Cottle et al. does not affect the patentability of Claim 6.

Merely because Cottle et al. may store OSD data in multiple memories does not suggest the transfer of OSD data between memories. Cottle et al. accesses OSD data from wherever it happens to reside. Nowhere does Cottle et al. teach or suggest the transfer of OSD data between memories, much less the transfer of OSD data that is no longer being displayed from the second memory to the first memory as specifically recited in Claim 6.

Furthermore, nowhere does Cottle et al. show or suggest transferring OSD data that is no longer being displayed to the first memory which is used for video decompression, as specifically recited in Claim 6.

Claims 7-9 are dependent from Claim 6 and add further advantageous features. The Applicants submit that these subclaims are patentable as their parent Claim 6.

The Applicants believe that the instant application is now in condition for allowance. A notice to that effect is respectfully solicited.

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